

Notice of Allowability	Application No.	Applicant(s)
	10/692,741	YU, JIAN-SHEN
	Examiner	Art Unit
	VIJAY SHANKAR	2629

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTO-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to RCE and Amendment filed on 9/19/07.
2. The allowed claim(s) is/are 1-16.
3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some* c) None of the:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. Notice of References Cited (PTO-892)
2. Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date _____
4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. Notice of Informal Patent Application
6. Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. Examiner's Amendment/Comment
8. Examiner's Statement of Reasons for Allowance
9. Other _____.



VIJAY SHANKAR
Primary Examiner
Art Unit: 2629

DETAILED ACTION

Continued Examination Under 37 CFR 1.114

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 9/19/07 has been entered.

Allowable Subject Matter

2. Claims 1-16 are allowed.

3. The following is an examiner's statement of reasons for allowance: The prior arts fails to teach a driving circuit for a flat display panel, the flat display panel including a display area, comprising: a first video signal line for providing video signals; a second video signal line for providing video signals; a third video signal line for providing video signals; at least one buffer unit for inverting a scanning signal;

a first switch unit disposed between the first video signal line and the third video signal line; a second switch unit disposed between the first video signal line and the third video signal line; a third switch unit disposed between the first video signal line and the third video signal line, and the first, second, third switch units and the display area of the flat display panel being spaced apart with the third video signal line being between the switch units and the display area; and wherein the first switch unit is connected to the first video signal line to receive a

video signal and is connected to the buffer unit to receive the scanning signal inverted by the buffer unit, the second switch unit is connected to the second video signal line to receive a video signal and is connected to the buffer unit to receive the scanning signal inverted by the buffer unit, the third switch unit is connected to the third video signal line to receive a video signal and is connected to the buffer unit to receive the scanning signal inverted by the buffer unit, and the scanning signal controls output of the video signals by the first switch unit, the second switch unit, and the third switch unit to the display area of the flat display panel as claimed in Claim 9.

4. The prior arts fails to teach a driving circuit for a flat display panel, the flat display panel including a display area, comprising: a plurality of video signal lines for providing video signals; at least one buffer unit for inverting a scanning signal; and a plurality of switch units disposed between the plurality of video signal lines, and the plurality of switch units and the display area of the flat display panel being spaced apart with at least one video signal line between the switch units and the display area, wherein each of the plurality of switch units is connected to at least one video signal line to receive a video signal and is connected to the buffer unit to receive the scanning signal inverted by the buffer unit, and the scanning signal controls output of the video signals by the plurality of switch units to a-the display area of the flat display panel as claimed in claim 1.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably

accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to VIJAY SHANKAR whose telephone number is (571)272-7682. The examiner can normally be reached on M-F 7:00 am - 5:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, BIPIN SHALWALA can be reached on (571) 272-7681. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.



VIJAY SHANKAR
Primary Examiner
Art Unit 2629